DSX

The next level of High Speed Tester (HST)

ARC Technology Solutions' DSX is an FPGA-Based unit can be used for high speed processing of digital signals. DSX's is widely applicable in many different solutions. This system can be used to replace legacy systems that are difficult to service and have obsoleted parts. Make DSX a part of your complex signal system today.

SPECIFICATIONS:

» FPGA-based hardware

» 96 programmable digital I/O pins, 3.3V logic

» 24 programmable LVDS pins

» Programmable clock outputs up to 100MHz

» External clock input

» Common applications including digital testing, control, and data processing for custom or embedded products

FEATURES AND BENEFITS:

» Easily replace Legacy hardware
  • 74 Series Obsolete Logic Designs
  • ADC/DDC Interfaces
  • Memory/Double E-Prom Interfaces
  • Any Analog/Digital Interfaces

» Daisy Chain Multiple Units

» Expandable systems

» Stream data to PC over USB

» Supports LabVIEW for HST IP

» Provides platform for digital testing and laboratory prototyping

Digital System X

ARC
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